

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: Chang  
Application Number: 10/823,513  
Filing Date: April 12, 2004  
Title: Autorun for Integrated Circuit Memory Component

Examiner: Rampuria, Satish  
Art Unit: 2191

**Petition to Accept an Unintentionally Delayed Claim under 35  
U.S.C. 119(e), 120, 121, or 365(c) for the Benefit of a Prior-Filed  
Application**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Applicant hereby petitions for acceptance of an unintentionally delayed claim under 35 U.S.C. 119(e), 120, 121, or 365(c) for the benefit of a prior-filed application.

- (i) A reference required by 35 U.S.C. 120 or 119(e) and 37 CFR 1.78(a)(2) or (a)(5) to the prior-filed application has been incorporated into the application by an amendment filed herewith, and
- (ii) The surcharge set forth in 37 CFR 1.17(t) is submitted.

The entire delay between the date the claim was due under paragraph 37 CFR 1.78(a)(2)(ii) of this section and the date the claim was filed was unintentional.

IPSOLON LLP  
111 SW COLUMBIA #710  
PORTLAND, OREGON 97201  
TEL. (503) 249-7066  
FAX (503) 249-7068

Respectfully Submitted,



Mark M. Meininger  
Registration No. 32,428